Page 9, line 21, delete "comprise" and insert therefor --be in the form of--.

Page 11, line 1, delete "CLAIMS" and insert therefor --What is claimed is:--.

IN THE CLAIMS:

Kindly amend the claims as follows:

1. (Amended) A method for the wireless transmission of data between one computer and at least one [or more] other [computers] computer with the aid of [the DAB] a digital transmission system [or a corresponding system] for the wireless transmission of digital data, where [the] a transmitting computer is connected to a [DAB] digital transmitter and where [the] a receiving computer [or computers is/are] is connected to a respective [DAB] digital receiver, [characterized in that information that is outputted intermittently from the transmitting computer (1) is stored intermediately] said method comprising the steps of: storing information to be transmitted from the transmitting computer to the receiving computer in a first memory [(8) of] associated with a first adaptation circuit [(7)] disposed between the transmitting computer [(1)] and the [DAB] digital transmitter [(3); in that information is outputted essentially] ; substantially continuously outputting information from said first memory [(8)] to said [DAB] digital transmitter [(3)] under the control of an outfeed oscillator [(9)] in the first adaptation circuit; [in that transmitted] transmitting digital information [is received by a DAB] from the digital transmitter to a digital receiver [(4) and fed] operatively coupled with the receiving computer; feeding the digitally transmitted information from the digital receiver into a second memory [(12) in] associated with a second adaptation circuit [(11)] disposed between the digital

receiver and the receiving computer and that is under the control of an infeed oscillator [(13)] in the second adaptation circuit [(11); in that] ; storing the digitally transmitted information in the second memory; operating the two oscillators [(9, 13) operate on mutually] at substantially the same frequency [or essentially the same frequency; and in that the receiving computer (2) is caused to fetch information] ; and conveying intermittently from the second memory [(12)] in the second adaptation circuit [(11)] to the receiving computer nformation that has been stored in the second memory.

2. (Amended) A method according to Claim 1, [characterized in that] including the step of synchronizing the frequency of the infeed oscillator [(13)] in the second adaptation circuit [(11) is caused to be synchronized] with the frequency of the outfeed oscillator [(9)] in the first adaptation circuit [(7),] by locking the frequency of the [second] infeed oscillator [(13)] onto a reference included in the [received] digitally transmitted signal from the digital rtransmitter.

3. (Amended) A method according to Claim 1 [or 2, characterized in that] , including the steps of providing in the second adaptation circuit [(11) includes] a microprocessor [(15) which is caused to determine] for determining from a fast information channel (FIC) in the [DAB] digital system those parts of the [received] digitally transmitted signal that contain data, and [to cause] storing the [received] digitally transmitted data [to be stored] in the second memory [(12) of the second adaptation circuit (11)].

4. (Amended) A method according to Claim 3, [characterized in that] including

the step of identifying in the microprocessor [(15)] of the second adaptation circuit [(11) is caused to identify] information that is relevant to a receiving computer [(2)] and that includes identification of address information [and possibly also authorization].

5. (Amended) An arrangement for the wireless transmission of data between a first computer and at least one [or more] other [computers] computer with the aid of [the DAB system or some corresponding] a digital transmission system for the wireless digital transmission of data, [where the] said arrangement comprising: a transmitting computer [is] connected to a [DAB] digital transmitter [and where the] ; a receiving computer [or computers is/are] connected to a [respective DAB] digital receiver [, characterized by] ; a first adaptation circuit [(7)] disposed between [a] the transmitting computer [(1)] and the [DAB] digital transmitter [(3)], said first adaptation circuit [being] adapted to store information delivered intermittently from the transmitting computer [(1) intermediately] in a first memory [(8) that belongs to] associated with said first adaptation circuit [(7); in that] , wherein the first adaptation circuit [(7) is adapted to output] outputs the information from said first memory [(8)] to said [DAB] digital transmitter [(3)] [essentially] substantially continuously [under the control of] ; an outfeed oscillator [(9)] disposed in the first adaptation circuit [(7)]; [in that the arrangement includes] a second adaptation circuit [(11)] disposed between [DAB] the digital receiver [(4)] and the receiving computer [(2) respectively, said second adaptation circuit (11) being adapted to input information received by the DAB receiver (4) into] ; a second memory [(12)] disposed in the second adaptation circuit [(11) under the control of] , wherein said second adaptation circuit inputs information received by the digital receiver into the second memory; an infeed oscillator [(13)]

disposed in said second adaptation circuit [(11); in that] for controlling the transmission of information from the second memory to the receiving computer, wherein the [two] outfeed and infeed oscillators [(9, 13)] operate at [the same or essentially] substantially the same frequency; and [in that] wherein the receiving computer [(2) is adapted to] can fetch information intermittently from the second memory [(12) in the second adaptation circuit (11)].

6. (Amended) An arrangement according to Claim 5, [characterized in that] wherein the frequency of the infeed oscillator [(13)] in the second adaptation circuit [(11) is intended to be] is synchronized with the frequency of the outfeed oscillator [(9)] in the first adaptation circuit [(7),] by locking the frequency of the [second] infeed oscillator [(13)] to a reference signal included in the [received] transmitted signal.

7. (Amended) An arrangement according to Claim 5 [or 6, characterized in that], wherein the second adaptation circuit [(11)] includes a microprocessor [(15) which is adapted to decide] for determining from a fast information channel (FIC) in the [DAB] digital system which parts of the [received] transmitted signal contain data, and to store [received] transmitted data in the second memory [(12) of the second adaptation circuit (11)].

8. (Amended) An arrangement according to Claim 7, [characterized in that] wherein the microprocessor [(15)] in the second adaptation circuit [(11) is adapted to identify] identifies transmitted information that is relevant to the receiving computer [(2)] and that includes identification of address information [and possibly also authorization].